Hardware Laboratory
Dr. Vickery

CSCI 381.3/780

Section E6MDA

Reg. Codes 3113/3114

Class Meetings

- Four 50-minute hours per week.
  - Approximately Half lectures, Half closed labs
  - Lectures: Remsen 105
    - Mondays
  - Labs: SB A-227
    - Wednesdays
- Open Labs
  - SB A-205
    - Get Key From Office or Main Gate

Course Material

- No Textbook

- Lots of PDF files

- Lab Software is Licensed, but licenses can be checked out from off-campus.
  - License server (oak.cs.qc.edu) must be running.

Course Grade

- Laboratory Exercises
  - Tutorial in nature
- Final Project
  - Your choice
  - May work in teams
  - Present to class during final week.

Course Content

◆ FPGA Project Development using the Handel-C Hardware Implementation Language, the Celoxica DK Development Platform, Xilinx ISE Foundation Software Package, and the Celoxica RC200E prototyping kit.

Lab Demonstrations

- The RC200E Capabilities

- Using DK to simulate a design.

- Using DK and ISE to implement a design and download it to an RC200E.